CLAIMS

1. A method for managing memory in a video signal processing device comprising:

disabling a first memory and a second memory;

switching an output from said first memory to said second memory in response to a portion of a video signal; and

enabling said first memory and said second memory.

2. The method for managing memory of claim 1 wherein said portion of a video signal is a video blanking interval.

3. The method for managing memory of claim 2, wherein said video blanking interval is a vertical video blanking interval.

4. The method for managing memory of claim 1 wherein said output is connected to a video filter.

5. The method for managing memory of claim 4 wherein said first memory and said second memory store video filter coefficient data.

6. The method for managing memory of claim 5 wherein said video filter coefficient data is the memory address data of video filter coefficients.

7. The method for managing memory of claim 1 wherein disabling said first memory and said second memory comprises the steps of disabling the read and write functions of said first memory and said second memory.

8. A method for changing video filter coefficients in a video signal processing device comprising:

detecting a change in a video display format of a video signal;

writing at least one address of a bank of video filter coefficients to a first memory;

disabling said first memory;

switching an output of a second memory to said first memory in response to a portion of a video signal; and

enabling said first memory.

9. The method for changing video filter coefficients in a video signal processing device of claim 8 wherein said portion of a video signal is a video blanking interval.

10. The method for changing video filter coefficients in a video signal processing device of claim 9, wherein said video blanking interval is a vertical video blanking interval.

11. The method for changing video filter coefficients in a video signal processing device of claim 1 wherein said output is connected to a video filter.

12. The method for changing video filter coefficients in a video signal processing device of claim 4 wherein said first memory and said second memory store video filter coefficient data.

13. An apparatus for selecting one of a plurality of video filter coefficients comprising:

a first memory for storing a first set of video filter data;

a second memory for storing a second set of video filter data;

a switch (422) for selecting either said first memory or said second memory; and

a bank switching device for detecting a portion of a video signal and changing the state of said switch.

14. The apparatus of claim 13 wherein said portion of a video signal is a video blanking interval.

15. The apparatus of claim 14, wherein said video blanking interval is a vertical video blanking interval.

16. The apparatus of claim 13 wherein said first set of video filter data and said second set of video filter data are a plurality of memory address locations of video filter coefficients.

17. The apparatus of claim 13 wherein said first set of video filter data and said second set of video filter data are a plurality of video filter coefficients.

18. The apparatus of claim 13 wherein said switch is a multiplexer.

19. The apparatus of claim 13 wherein said apparatus is included within an integrated circuit.